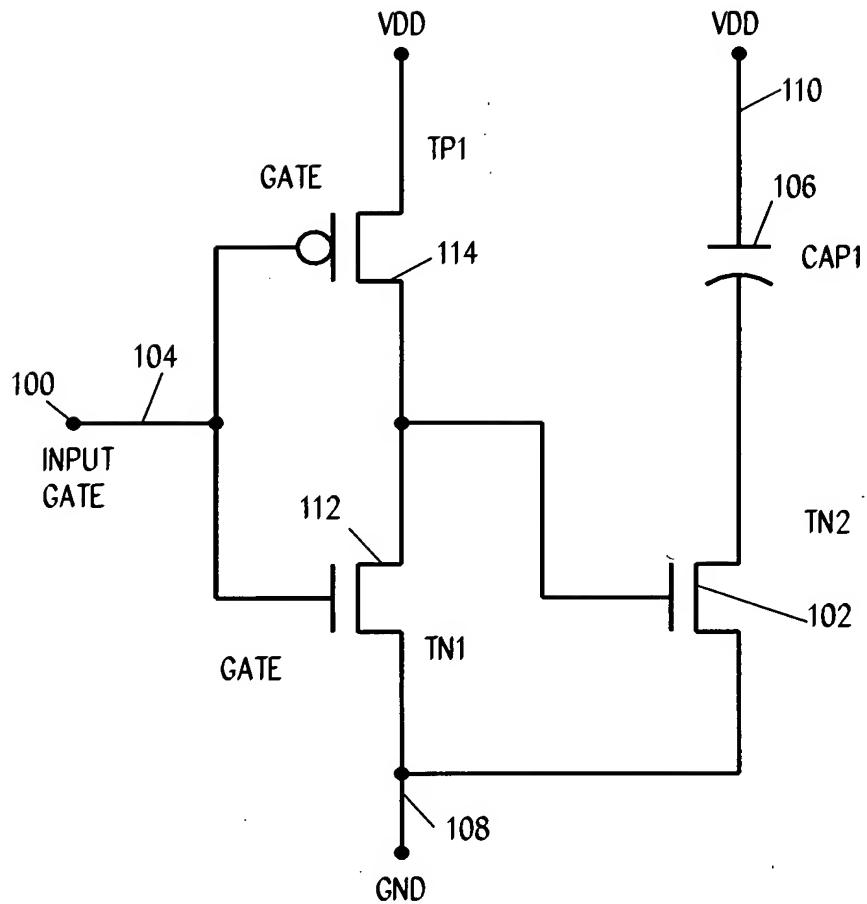




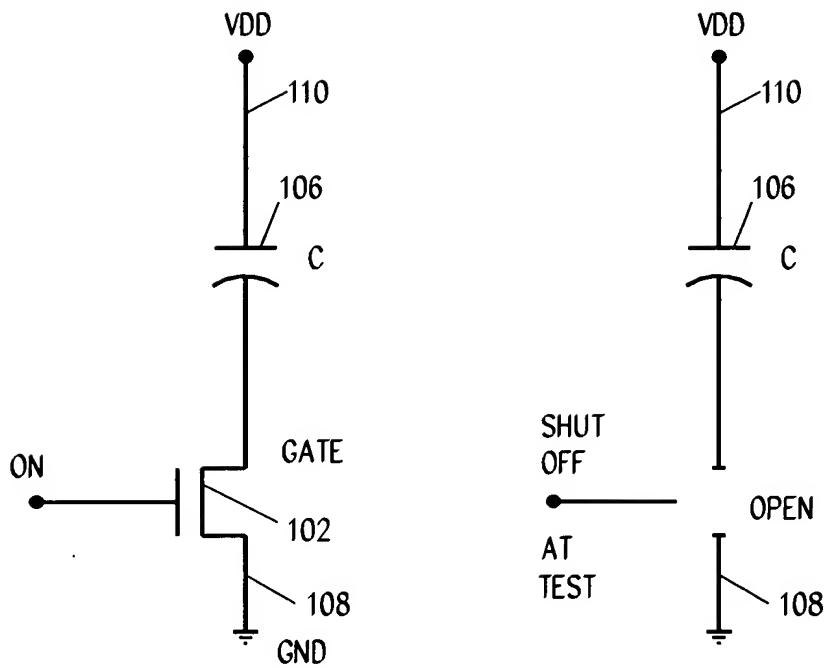
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DECOUPLING CAP WITH NFET GATE

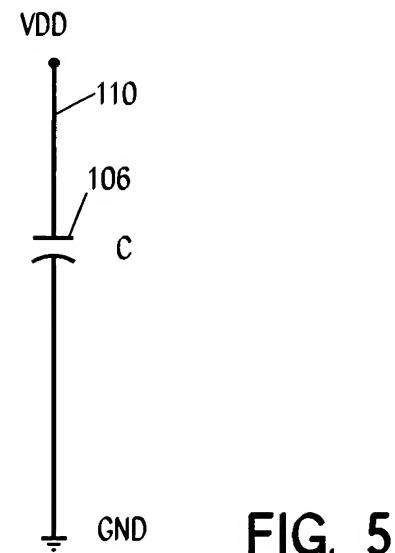
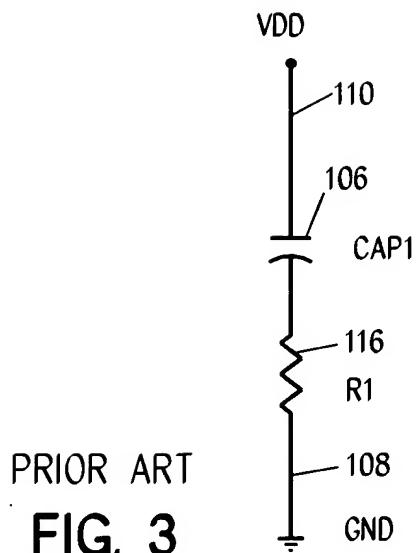
PRIOR ART

**FIG. 1**

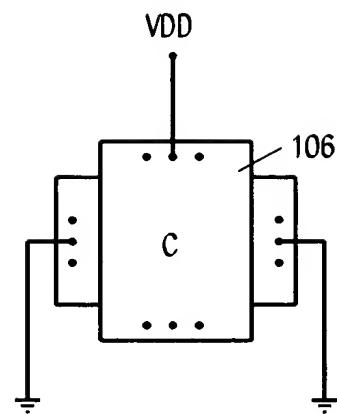
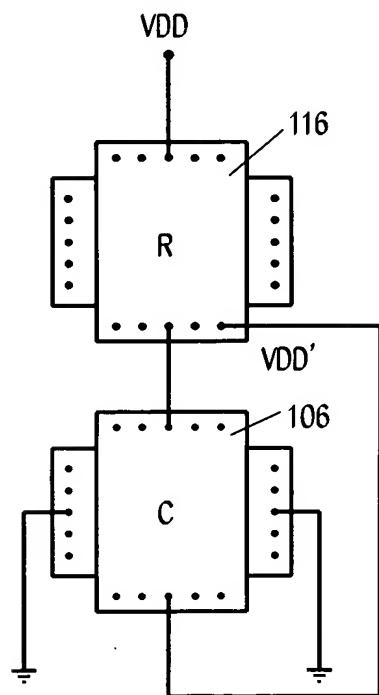


PRIOR ART

**FIG. 2**



DECOUPLING CAP WITH  
INTEGRATED RESISTOR



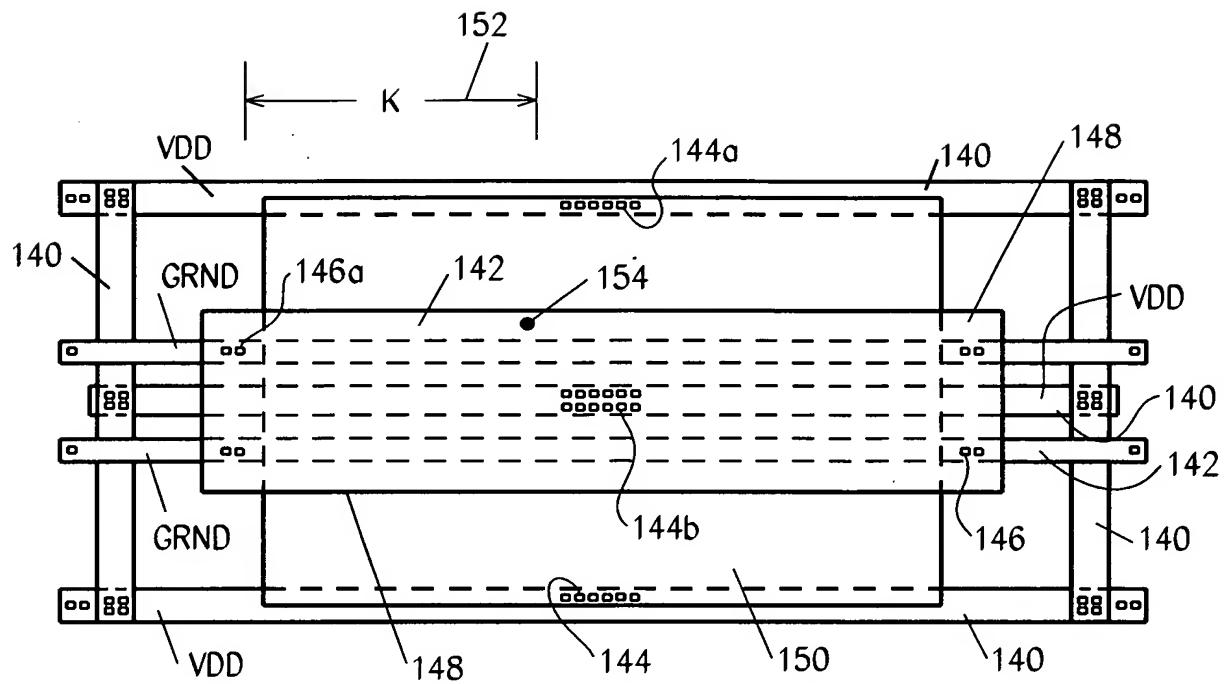


FIG. 7

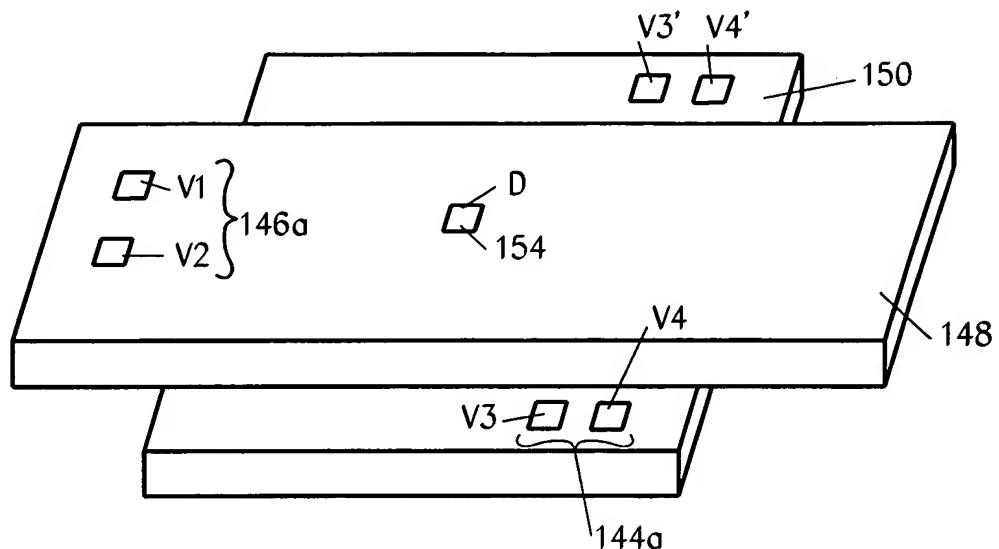


FIG. 8

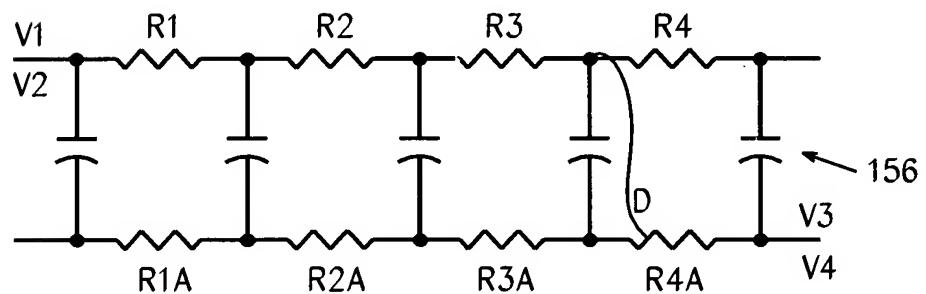
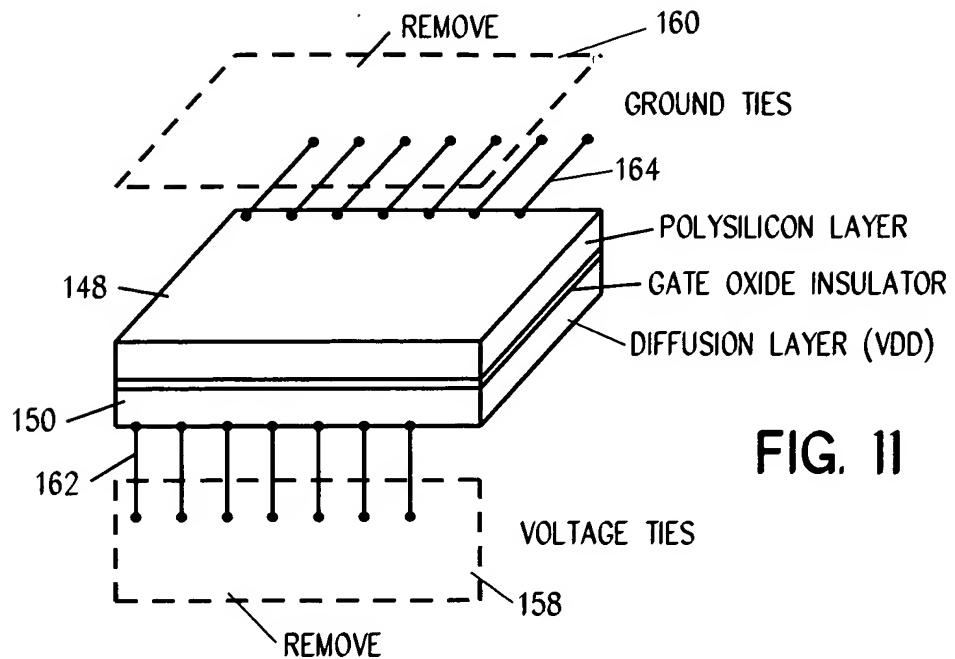
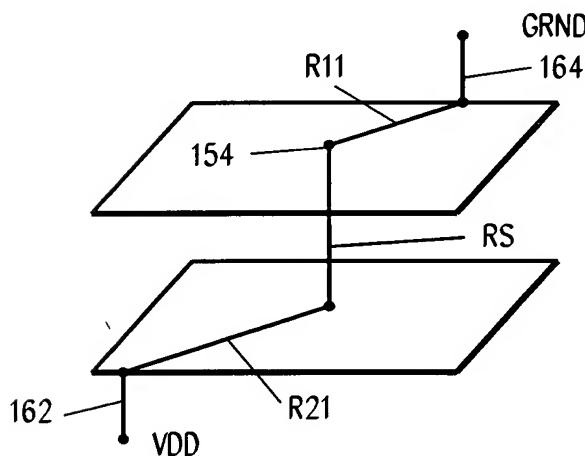


FIG. 9



**FIG. 11**



**FIG. 12**

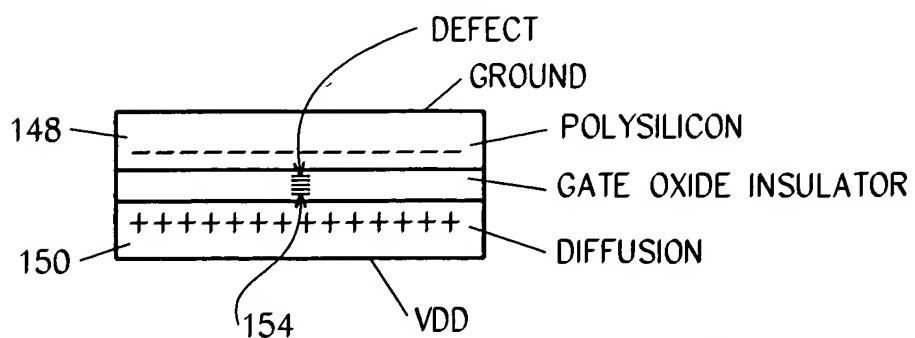


FIG. 10

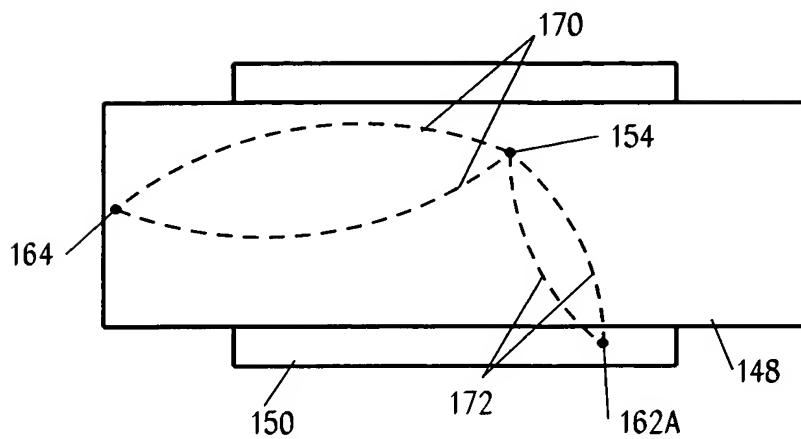


FIG. 13

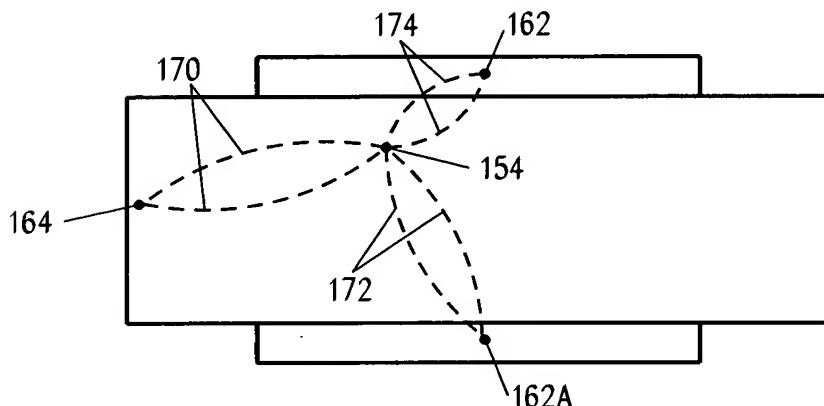


FIG. 14

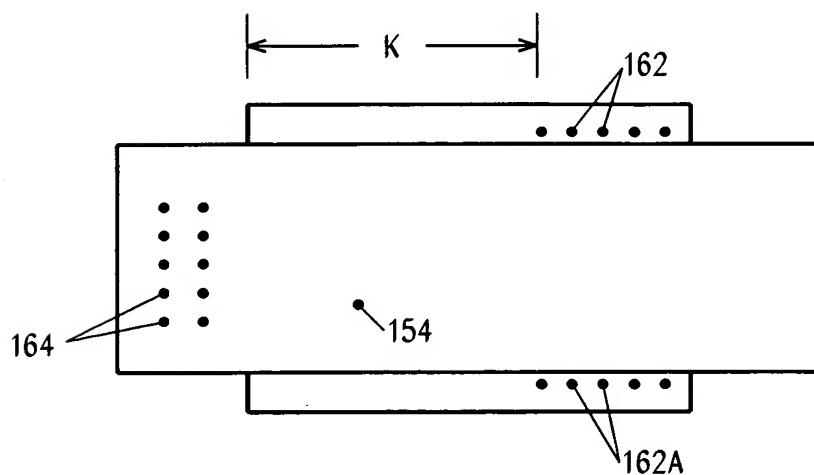


FIG. 15

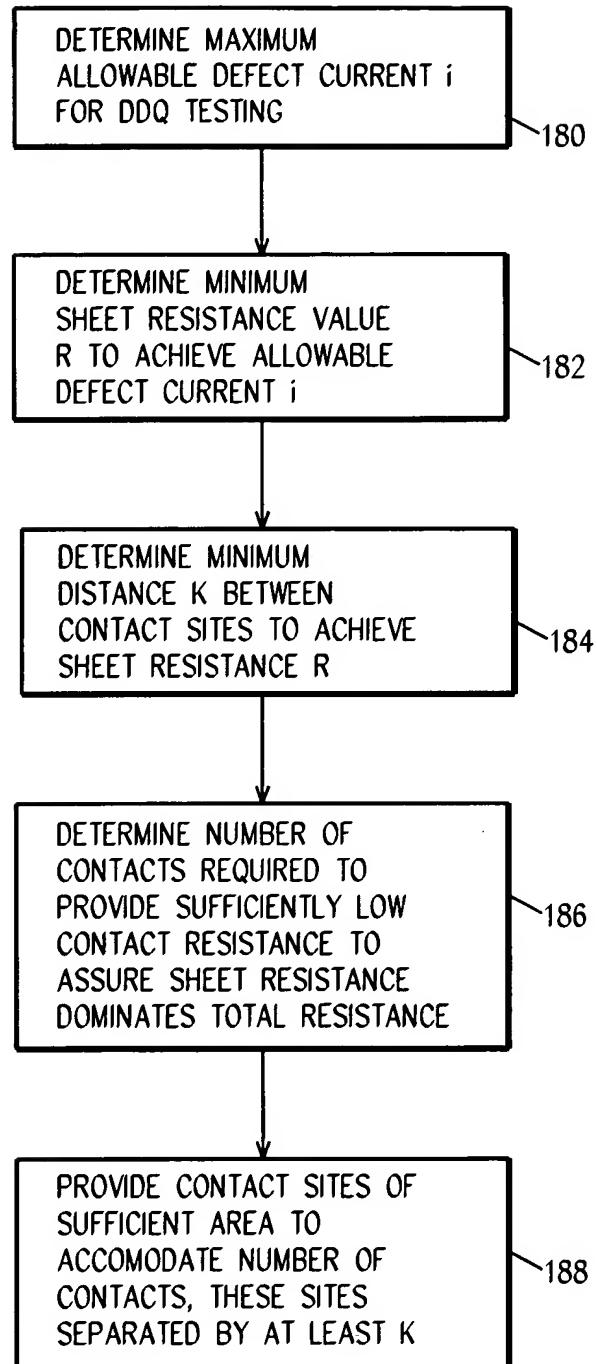


FIG. 16

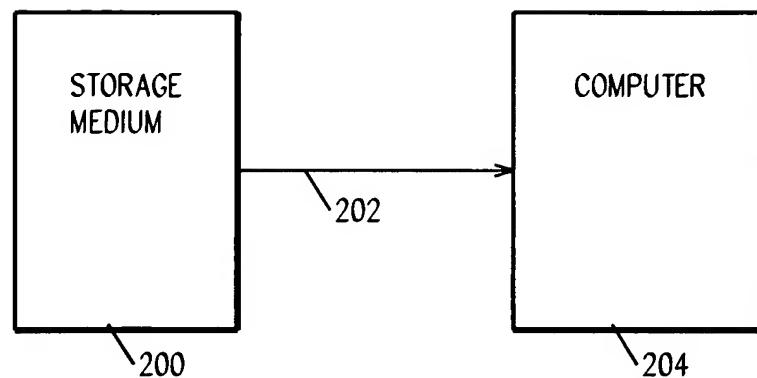


FIG. 17